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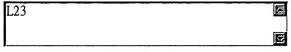
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<u>L21</u>	L17 and redundan\$5	5	<u>L21</u>
<u>L20</u>	L8 and redundan\$5	52	<u>L20</u>
<u>L19</u>	L18 and redundan\$5	15	<u>L19</u>
<u>L18</u>	116 and count\$3	183	<u>L18</u>
<u>L17</u>	(multi near1 thread\$4 or thread\$3) near5 (plur\$7 or multiple or two) and 14	23	<u>L17</u>
<u>L16</u>	(multi near1 thread\$4 or thread\$3) near5 (plur\$7 or multiple or two) and 13	287	<u>L16</u>
<u>L15</u>	L1 and l4	6	<u>L15</u>
<u>L14</u>	L1 and 16	0	<u>L14</u>
<u>L13</u>	L8 and 16	0	<u>L13</u>
<u>L12</u>	L8 and 15	2	<u>L12</u>
<u>L11</u>	L8 and 14	0	<u>L11</u>
<u>L10</u>	L8 and 13	17	<u>L10</u>
<u>L9</u>	L8 and 12	44	<u>L9</u>
<u>L8</u>	L7 and count\$5 near6 (cycle\$1 or clock\$3 or tim\$5)	201	<u>L8</u>
<u>L7</u>	(lead\$3 or trail\$4 or first or primary or second) and 11	2762	<u>L7</u>
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<u>L6</u>	(714/6)![CCLS]	1287	<u>L6</u>
<u>L5</u>	(717/163)![CCLS]	107	<u>L5</u>
<u>L4</u>	(719/331)![CCLS]	263	<u>L4</u>
<u>L3</u>	(718/107,108)![CCLS]	771	<u>L3</u>
<u>L2</u>	(712/3-300)![CCLS]	10293	<u>L2</u>
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<u>L1</u>	(multi near1 thread\$4 or thread\$3) near5 (plur\$7 or multiple or two) and (replicat\$4 or duplicat\$4 or cop\$3 or identical) near4 thread\$3	3047	<u>L.1</u> .

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Computer Software and Applications Conference, 2002. COMPSAC 2002. Proceedings. 26th Annual International, 26-29 Aug. 2002 Pages:757 - 762

### 4 A study of slipstream processors

[PDF Full-Text (417 KB)]

Purser, Z.; Sundaramoorthy, K.; Rotenberg, E.; Microarchitecture, 2000. MICRO-33. Proceedings. 33rd Annual IEEE/ACM International Symposium on , 10-13 Dec. 2000 Pages: 269 - 280

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